# ⚡ Logic Probe

| Parameter / Model | SPL2016 | Parameter / Model | SPL1016 |
|---|---|---|---|
| |  | |  |
| Channels | 16 | Channels | 16 |
| Input Impedance | 100 KΩ‖18 PF | Input Impedance | 100 KΩ‖8 pF |
| Max. Input Voltage | ±50 V peak | Max. Input Voltage | ±20 V peak |
| Threshold Groupings | Pod 2: D15-D8<br>Pod 1: D7-D0 | Threshold Groupings | Pod 2: D15-D8<br>Pod 1: D7-D0 |
| Threshold Selections | TTL (1.5 V), COMS (2.5 V) , 3.3 V_LVCOMS (1.65 V), 2.5 V_LVCOS (1.25 V) | Threshold Selections | TTL (1.5 V), COMS (2.5 V), 3.3 V_LVCOMS (1.65 V), 2.5 V_LVCOS (1.25 V) |
| User defined threshold range | ±3 V in 10 mv steps | User defined threshold range | ±3 V in 10 mv steps |
| Threshold Accuracy | ± ( 3%*Threshold+200 mv ) | Threshold Accuracy | ± ( 3%*Threshold+150 mv ) |
| Input Dynamic Range | ±20 V | Input Dynamic Range | ±10 V |
| Min. Input Voltage Swing | 800 mVp | Min. Input Voltage Swing | 800 mVpp |
| Max. Input Frequency | Probe: 150 MHz<br>Line: 50 MHz | Max. Input Frequency | 60 MHz |
| Min. Detectable Pulse Width | 3.3 ns | Min. Detectable Pulse Width | 8.3 ns |
| Channel Skew | ±(1 digital sample interval) | Channel Skew | ±(1 digital sample interval) |